Search History

(HLAPHUS INSPEC, JORGAL, WHATAH) 3/20/66

(FILE 'HOME' ENTERED AT 11:16:06 ON 22 MAR 2006)

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FILE 'HCAPLUS, INSPEC, JAPIO, INPADOC, USPATFULL, USPAT2' ENTERED AT
     11:16:24 ON 22 MAR 2006
L1
            261 S (ARRAY#) (8A) (OPTO (W) ELECTRONIC (W) DEVICE#)
L2
              6 S (SEPARAT? OR PULL? OR LIFT?) (8A) (MULTILAYER#(4A) EPITAXIAL(W) F
L3
          97711 S (PLURAL? OR MULTIP?) (8A) (SEGMENT#)
          12042 S (CONFIN? OR SEGREGAT? OR SEPARAT?) (8A) (ACTIV? (6A) REGION# OR A
L4
L5
              2 S L1 AND L2 AND L3 AND L4
L6
              3 S L1 AND L3 AND L4
=> d 16 1-3 abs,bib
L6
     ANSWER 1 OF 3 HCAPLUS COPYRIGHT 2006 ACS on STN
AΒ
     An opto-electronic device array is
     made from a multilayer epitaxial film by the following steps. /
     multilayer epitaxial film is separated into a plurality of
     segments. The segments are transferred to a 1st substrate to be
     arranged in an array substantially. / Active regions /
     are resp. confined in the segments so that the adtive
     regions form the array'.
AN
     2004:569484 HCAPLUS
DN
     141:131048
TI
     Production method of optofelectr
                                            device
     array
     Furuyama, Hideto
IN
                                  apan; New Energy and Industrial Tech.
PΑ
     Kabushiki Kaisha Toshi
     Development Org.
SO
     U.S. Pat. Appl. Publ.,
                            \22 pp
     CODEN: USXXCO
DT
     Patent
     English
T.A
FAN.CNT 1
                                 DATE
     PATENT NO.
                                              APPLICATION NO
                          KIND
     US 2004134416
                           Aγ
                                 2Q040715
                                              US 2003-743087
                                                                        0031223
     JP 2004207388
                           A2
                                 20040722
                                              JP 2002}37/2203
                                                                      20021224
                                 20051207
     JP 3723177
                           B2
                                 20021224
PRAI JP 2002-372903
                           Α
L<sub>6</sub>
      ANSWER 2 OF 3
                      INPADOC
                               COPYRIGHT
                                               ÉPO on STN
LEVEL 1
      238762487 INPADOC
                          ED 20040806 EW 200432 UP 20041028 UW 200444
AN
TI
      Production method of opto-electronic device
      array.
IN
      FURUYAMA HIDETO
INS
      FURUYAMA HIDETO
INA
      KABUSHIKI KAISHA TOSHIBA; NEW ENERGY AND INDUSTRIAL TECH. DEVELOPMENT
PA
PAS
      TOKYO SHIBAURA ELECTRIC CO; NEW ENERGY AND IND TECH DEV OR
PAA
      JP; JP
      English
TL
DT
      Patent
PIT
      USAA PATENT APPLICATION PUBLICATION (PRE-GRANT)
PI
      US 2004134416
                           AA 20040715
ΑI
      US 2003-743087
                            A 20031223
PRAI
      JP 2002-372903
                            A 20021224
                                            (EDPR 20040806)
OSDW 2004-579087
L6
     ANSWER 3 OF 3 USPATFULL on STN
       An opto-electronic device array
AB
       is made from a multilayer epitaxial film by the following steps. The
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multilayer epitaxial film is separated into a plurality of

segments. The segments are transferred to a first

substrate to be arranged in an array substantially. Active regions are respectively confined in the segments so that the active regions form the array.

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CAS INDEXING IS AVAILABLE FOR THIS PATENT.
AN
       2004:175264 USPATFULL
TI
       Production method of opto-electronic device
       Furuyama, Hideto, Yokohama-shi, JAPAN
IN
       KABUSHIKI KAISHA TOSHIBA, Tokyo, JAPAN (non-U.S. corporation)
PA
       NEW ENERGY AND INDUSTRIAL TECH. DEVELOPMENT ORG., Tokyo, JAPAN (non-U.S.
       corporation)
PΙ
       US 2004134416
                               20040715
                          A1
       US 2003-743087
ΑI
                         A1
                               20031223 (10)
       JP 2002-372903
PRAI
                           20021224
\mathtt{DT}
       Utility
       APPLICATION
FS
       OBLON, SPIVAK, MCCLELLAND, MAIER & NEUSTADT, P.C., 1940 DUKE STREET,
LREP
       ALEXANDRIA, VA, 22314
CLMN
       Number of Claims: 20
ECL
       Exemplary Claim: 1
DRWN
       13 Drawing Page(s)
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LN.CNT 793

CAS INDEXING IS AVAILABLE FOR THIS PATENT.

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1.1

L2

L3

L4

L5

ΑN TΤ

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DRWN

LN.CNT 793

13 Drawing Page(s)

CAS INDEXING IS AVAILABLE FOR THIS PATENT.

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     FILE 'HCAPLUS, INSPEC, JAPIO, INPADOC, USPATFULL, USPAT2' ENTERED AT
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            261 S (ARRAY#) (8A) (OPTO(W) ELECTRONIC(W) DEVICE#)
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          97711 S (PLURAL? OR MULTIP?) (8A) (SEGMENT#)
          12042 S (CONFIN? OR SEGREGAT? OR SEPARAT?) (8A) (ACTIV? (6A) REGION# OR A
=> s 11 and 12 and 13 and 14
             2 L1 AND L2 AND L3 AND L4
=> d l5 1-2 abs,bib
      ANSWER 1 OF 2 INPADOC COPYRIGHT 2006 EPO on STN
LEVEL 1
      238762487 INPADOC ED 20040806 EW 200432 UP 20041028
      Production method of opto-electronic device
      array.
      FURUYAMA HIDETO
      FURUYAMA HIDETO
INS
INA
      KABUSHIKI KAISHA TOSHIBA; NEW ENERGY AND INDUSTRIAL
                                                                  EVELOPMENT
                                                  IND TECH DEV
PAS
      TOKYO SHIBAURA ELECTRIC CO; NEW ENERGY AND
      JP; JP
PAA
      English
      Patent
      USAA PATENT APPLICATION PUBLICATION
                                               -GRANT)
PIT
      US 2004134416
                           AA 2004071
                              (2003\1223
      US 2003-7430/87
      JP 2002-372903
                              20021224
PRAI
                                            EDPR 20040806
     2004-579087
OSDW
     ANSWER 2 OF 2 USPATFULL on STN
       An opto-electronic device array
       is made from a multilayer epitaxial fix mby
                                                   the following steps. The
       multilayer epitaxial film is
       separated into a plurality of segments. The
       segments are transferred to a fixet substrate to be arranged in
       an array substantially. Active regions are
       respectively confined in the segments so that the
       active regions form the array.
CAS INDEXING IS AVAILABLE FOR THIS PATENT.
       2004:175264 USPATFULL
       Production method of opto-electronic device
       array
       Furuyama, Hideto, Yokohama-shi, JAPAN
       KABUSHIKI KAISHA TOSHIBA, Tokyo, JAPAN (non-U.S. corporation)
       NEW ENERGY AND INDUSTRIAL TECH. DEVELOPMENT ORG., Tokyo, JAPAN (non-U.S.
       corporation)
       US 2004134416
                          Α1
                                20040715
       US 2003-743087
                          A1
                                20031223 (10)
       JP 2002-372903
                           20021224
PRAI
       Utility
       APPLICATION
LREP
       OBLON, SPIVAK, MCCLELLAND, MAIER & NEUSTADT, P.C., 1940 DUKE STREET,
       ALEXANDRIA, VA, 22314
CLMN
       Number of Claims: 20
       Exemplary Claim: 1
ECL
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Day: Wednesday

Date: 3/22/2006 Time: 10:51:51



Inventor Name Search Result

Your Search was:

Last Name = FURUYAMA First Name = HIDETO

Application#	Patent#	Status	Date Filed	Title	Inventor Name	
06806497	<u>4676863</u>	150	12/09/1985	METHOD OF FABRICATING A MESA STRIPE ON A SEMICONDUCTOR WAFER PLANE	FURUYAMA, HIDETO	
06810093	4701774	150	12/18/1985	II \ \ \ \ \	FURUYAMA, HIDETO	
<u>06943760</u>	<u>4810670</u>	150	12/19/1986	METHOD OF MANUFACTURING AN EMBEDDED TYPE SEMICONDUCTOR LASER	FURUYAMA, HIDETO	
07095114	4870468	150	09/11/1987	SEMICONDUCTOR LIGHT- EMITTING DEVICE AND METHOD OF MANUFACTURING THE SAME	FURUYAMA, HIDETO	
<u>07198859</u>	4858241	150	05/26/1988	SEMICONDUCTOR LASER DEVICE	FURUYAMA, HIDETO	
07198866	4862474	150	05/26/1988	SEMICONDUCTOR LASER DEVICE	FURUYAMA, HIDETO	
07257519	5084410	150		METHOD OF MANUFACTURING SEMICONDUCTOR DEVICES	FURUYAMA, HIDETO	
07382345	4958202	250	07/20/1989	1	FURUYAMA, HIDETO	
07383099	4974232	250		SEMICONDUCTOR LASER DEVICE	FURUYAMA, HIDETO	
07383100	4974233	250	07/21/1989		FURUYAMA, HIDETO	
07413489	4992386	150		1	FURUYAMA, HIDETO	

				SEMICONDDUCTOR LIGHT DETECTOR	
07554890	5060306	150	07/20/1990	OPTICAL TRANSMISSION SYSTEM	FURUYAMA, HIDETO
07582336	5221984	150	09/14/1990	OPTICAL DATA TRANSMISSION DEVICE WITH PARALLEL CHANNEL PATHS FOR ARRAYED OPTICAL ELEMENTS	FURUYAMA, HIDETO
07602174	5144381	150	10/23/1990	SEMICONDUCTOR LIGHT DETECTOR UTILIZING AN AVALANCHE EFRECT AND HAVING AN INPROVED GUARD RING STRUCTURE	FURUYAMA, HIDETO
07766204	5205032	150	11	ELECTRONIG PARTS MOUNTING APPARATUS	FURUYAMA, HIDETO
08118811	5434426	250	09/10/1993	OPTICAL INTERCONNECTION DEVICE	FURUYAMA, HIDETO
08120410	5343054	150	09/14/1993	SEMICONDUCTOR LIGHT- DETECTING DEVICE WITH RECOMBINATION RATES	FURUYAMA, HIDETO
08160919	5412748	150	12/03/1993	OPTICAL SEMICONDUCTOR MODULE	FURUYAMA, HIDETO
08379282	5559918	150		OPTICAL SEMICONDUCTOR MODULE IN WHICH A HERMETICALLY SEALED OPTICAL SEMICONDUCTOR DEVICE IS CONNECTED TO AN ELECTRICAL WIRING LAYER	FURUYAMA, HIDETO
08531640	5719979	150	09/21/1995	OPTICAL SEMICONDUCTOR MODULE AND METHOD FOR MANUFACTURING THE SAME	FURUYAMA, HIDETO
08917892	5980119	150	08/27/1997	SINGLE-CRYSTAL COMPONENT TO BE APPLIED TO OPTICAL MODELE AND ITS FABRICATION METHOD	FURUYAMA, HIDETO
09030959	6091147	150	02/26/1998	CONNECTOR TYPE SEMICONDUCTOR PACKAGE	FURUYAMA, HIDETO
09040342	5970200	150	03/18/1998	APPARATUS HAVING OPTICAL COMPONENTS AND A MANUFACTURING METHOD THEREOF	FURUYAMA, HIDETO

09408122	6487224	150	09/29/1999	LASER DIODE ASSEMBLY	FURUYAMA, HIDETO
09603896	6516104	150	06/26/2000	OPTICAL WIRING DEVICE	FURUYAMA, HIDETO
09606014	6449296	150	06/29/2000	SEMICONDUCTOR LASER DEVICE	FURUYAMA, HIDETO
09621482	6365911	150	07/21/2000	BIDIRECTIONAL SEMICONDUCTOR LIGHT- EMITTING EDEMENT AND OPTICAL SYSTEM	FURUYAMA, HIDETO
09749801	6587494	150	12/28/2000	SEMICONDUCTOR LASER DEVICE	FURUYAMA, HIDETO
09961372	6741781	150	09/25/2001	OPTICAL INTERCONNECTION CIRCUIT BOARD AND MANUFACTURING METHOD THEREOF	FURUYAMA, HIDETO
10090609	6654393	150	03/06/2002	SEMICONDUCTOR LASER DEVICE	FURUYAMA, HIDETO
10244386	6687272	150	09/17/2002	SEMICONDUCTOR LASER DEVICE	FURUYAMA, HIDETO
10305135	6760500	150	11/27/2002	OPTICAL WIRING DEVICE	FURUYAMA, HIDETO
10372914	6687277	150	02/26/2003	SEMICONDÚCTOR LASER DEVICE	FURUYAMA, HIDETO
10375353	6961523	150		OPTICAL MULTIPLEXING INTERCONNECT MODULE	FURUYAMA, HIDETO
10743087	Not Issued	30 Apr 1	12/23/2003 ants Inw	Production method of opto- electronic device array	FURUYAMA, HIDETO
10768123	6968109	150	02/02/2004	OPTICAL INTERCONNECTION CIRCUIT BOARD AND MANUFACTURING METHOD THEREOF	FURUYAMA, HIDETO
10778030	Not Issued	30	02/17/2004	LSI package provided with interface module and method of mounting the same	FURUYAMA, HIDETO
10898337	Not Issued	71	07/26/2004	Optical semiconductor module and its manufacturing method	FURUYAMA, HIDETO
10899154	Not Issued	30	07/27/2004	Wiring board and a semiconductor device using the same	FURUYAMA, HIDETO
10920365	Not Issued	30	1	Interface module-mounted LSI package	FURUYAMA, HIDETO



11012273	Not Issued	30	12/16/2004	Optical semiconductor module and method of manufacturing the same	FURUYAMA, HIDETO
11014833	Not Issued	30	12/20/2004	Holder of optical transmission lines and multi-core optical wave-guide	FURUYAMA, HIDETO
11015013	Not Issued	71	12/20/2004	LSI package provided with interface module	FURUYAMA, HIDETO
11049758	Not Issued	30	02/04/2005	Optical fiber connector and connecting method	FURUYAMA, HIDETO
11081617	Not Issued	30	03/17/2005	LSI package provided with interface module, and transmission line header employed in the package	FURUYAMA, HIDETO
11143731	Not Issued	93	06/03/2005	OPTICAL INTERCONNECTION CIRCUIT BOARD AND MANUFACTURING METHOD THEREOF	FURUYAMA, HIDETO
11197344	Not Issued	41		Optical multiplexing interconnect module	FURUYAMA, HIDETO
11200045	Not Issued	30	08/10/2005	Optical semiconductor module and semiconductor device including the same	FURUYAMA, HIDETO
11200173	Not Issued	30	08/10/2005	LSI package having interface function with exterior, circuit device including the same, and manufacturing method of circuit device	FURUYAMA, HIDETO
11203959	Not Issued	30	08/16/2005	LSI package equipped with interface module, interface module and connection holding mechanism	FURUYAMA, HIDETO

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Search Another: Inventor	Last Name	First Name	
Scaren Another: Inventor	Furuyama	Hideto	Search

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